

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO: 7,029,285
DATED: April 18, 2006
INVENTOR(S): Takashi ABE

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, line 41, after "in the memory bus", insert --110 D --

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